

PATENT NUMBER

**U.S. UTILITY Patent Application**



SCANNED

**O.I.P.E.**

**G.A.**

PATENT DATE

APPLICATION NO.  
09/651422

CONT/PRIOR

**CLASS**

SUBCLASS

ART UNIT

**EXAMINER**

## APPLICANTS

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Transistor structure, & their  
"methods of forming" (physically)

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PTO-2040  
12/99

**ISSUING CLASSIFICATION**

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		<b>NOTICE OF ALLOWANCE MAILED</b> _____	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	_____ (Primary Examiner)		<b>ISSUE FEE</b>	
	_____ (Date)		Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		<b>ISSUE BATCH NUMBER</b>	
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